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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/655,869	09/04/2003	Terence Quintin Collier	5036-0003	7306
7590	08/02/2004		EXAMINER	
Michael L. Diaz, P.C. Suite 200 555 Republic Drive Plano, TX 75074			TRINH, HOA B	
			ART UNIT	PAPER NUMBER
			2814	

DATE MAILED: 08/02/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/655,869

Applicant(s)

COLLIER, TERENCE QUINTIN

Examiner

Vikki H Trinh

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. ____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- ☒ Notice of References Cited (PTO-892)
- ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date ____.
- ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____.
- ☐ Notice of Informal Patent Application (PTO-152)
- ☐ Other: ____.

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1- 20 are rejected under 35 U.S.C. 102(e) as being anticipated by Gaku et al. (6,720,651)

Gaku et al. (6,720,651) discloses a method of bonding a multiplayer semiconductor package having the steps of :

As to claims 1, 9-10, 13-14, 17-18, perforating at least a first layer b with a plurality of vias h, I (col. 11, lines 7-20) aligning a second layer m (fig. 1 (7)), or e (fig. 5), or a (fig. 12) with the first layer such that the first layer has a surface adjoining and coplanar with a surface of the second layer; and applying attach material (o, f) between adjoining coplanar surfaces of the first and second layers and within the plurality of vias h, i such that a bond is formed between the layers wherein the vias provide a bonding surface. See fig. 11.

Note that the location for via interval was predetermined to provide heat diffusion and free of moisture absorption.

As to claims 2, 3, 15, the method further comprising the steps of coplanarly

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aligning at least one additional layer j, k, with the first or second layer; and
applying attach material between adjoining coplanar surfaces of
the aligned first or second layers (fig. 1, fig. 11) and at least one additional layers j or k or
e (fig. 1, fig. 11) such that a bond is formed between the layers to form a multi-layer
semiconductor package comprising three or more layers.

As to claim 4, the step of perforation is substantially perpendicular to the plane of
the perforated layer (fig. 1, fig. 11).

As to claim 5, the method includes soldering n (fig. 11).

As to claim 6, the method includes epoxing (col. 8, lines 1-10).

As to claim 7, the step of flowing the attach material o is by using a vacuum (col.
15, line 24) .

As to claims 8, 11, the step of laser drilling (col. 12, lines 35-37, col. 16, lines 34-
35).

As to claim 12, the step of etching (col. 12, line 66).

As to claim 16, the step of removing the temporary layer c (fig. 1) .

As to claims 18-20, the material forming between the two layers provides both a
thermal conduction or a dielectric for the package (col. 18, lines 45-60; col. 19, lines 3-5).

Conclusion

1. Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Vikki Trinh whose telephone number is (571) 272-1719. The Examiner can normally be reached Mon-Tuesday, Thurs-Friday, 7:30 AM - 6:00 PM

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Eastern Time. If attempts to reach the examiner by telephone are unsuccessful, the Examiner's supervisor, Mr. Wael Fahmy, can be reached at (571) 272-1705.

Vikki Trinh,
Patent Examiner
AU 2814



LONG PHAM
PRIMARY EXAMINER